

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant(s):** Stephen W. Bedell, et al.

**Examiner:** Unassigned

**Serial No:** Unassigned

**Art Unit:** Unassigned

**Filed:** Herewith

**Docket:** YOR920010499682US2  
(14710A)

**For:** METHOD OF CREATING  
HIGH-QUALITY RELAXED  
SiGe-ON-INSULATOR FOR STRAINED  
Si CMOS APPLICATIONS

**Dated:** April 14, 2004

Mail Stop Patent Application  
Commissioner for Patents  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Sir:

In connection with filing the above-identified application under 37 C.F.R.

§1.53(b), applicants submit the following amendments and remarks for consideration by the

Examiner and entry of record in the above-identified patent application.

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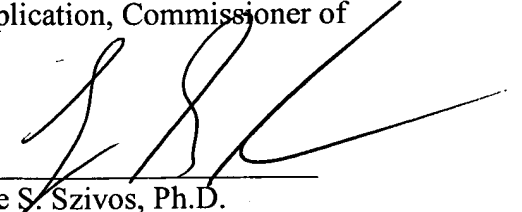
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Dated: April 14, 2004

  
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Leslie S. Szivos, Ph.D.